The International Test Conference (ITC) is the world’s premier venue dedicated to the electronic test of devices, boards, and systems—covering the complete cycle from design verification, design-for-test, design-for-manufacturing, silicon debug, manufacturing test, system test, diagnosis, reliability and failure analysis, and back to process and design improvement.

Emerging technologies in design and manufacturing will require new test solutions. Artificial Intelligence (AI) and the need for trustworthy devices are providing both new challenges and new opportunities for off-chip and on-chip test. At the same time, more stringent quality requirements, especially in automotive applications, are requiring more effective test, debug, monitoring, and repair techniques that can transfer to the field.

Authors are invited to submit original, unpublished papers describing recent work in the field of testing and testable design. Of particular interest are works dedicated to the topics listed on the right and/or works focused on special tracks such as Automotive, 5G/6G, AI, or Security. Authors are also invited to submit practical, industry-oriented papers. A special industrial case-study track is dedicated to papers that enable others to learn best industrial practices. Submissions simultaneously under review or accepted by another conference, symposium or journal, will be summarily rejected. The submission is through the EasyChair paper submission system.

Submissions must include:
- Title of paper.
- Name, affiliation, e-mail address of each author.
- The corresponding author(s). ITC will communicate with the corresponding author(s).
- One or two topic(s) from the topic list, or a description of your topic.
- An electronic copy of a complete paper of 6—10 pages for regular papers (including Industrial case-study papers) or 3—5 pages for short Industrial Practices papers.
- An abstract of 35 words or less to be entered online.

ITC maintains a competitive selection process for technical papers. Submissions must clearly describe the status of the reported work, its contribution, novelty and/or significance. Supporting data, results (priority is often given to papers with results from real designs) and conclusions, and references to prior work must also be included. ITC does not accept submissions that do not meet the specified criteria.

Paper title/abstract due: March 20, 2023
Paper final PDF due: April 3, 2023
Author notification: June 12, 2023
Final manuscript due: July 17, 2023

Authors are also invited to submit a single-page poster proposal. Posters are a useful way of presenting late-breaking results, getting feedback on an innovative method, or participating without having to write a full paper. Acceptance as a poster does not preclude submission of a more complete work as an ITC paper in 2024. Submit your ITC paper through EasyChair at https://easychair.org/conferences/?conf=itc2023. Additional information on poster submissions will be provided on the ITC web page.

Poster submission deadline: June 26, 2023
Author notification: July 10, 2023

Test Week tutorial and workshop proposals are also welcomed. Deadlines and other information about proposals can be obtained from TTTC at: http://tab.computer.org/ittc

For detailed information about the submission process, requirements and deadlines, the selection process and any other questions regarding the program itself or contact information, please consult the ITC web site at http://www.itctestweek.org.